

Computer & Reconfigurable Architectures

View Online



1

Stallings, William. Computer organization and architecture: designing for performance. 8th ed. Upper Saddle River, NJ: : Prentice Hall 2010.

2

Hennessy, John L., Patterson, David A. Computer architecture: a quantitative approach. 5th ed. Oxford: : Elsevier Science [distributor] 2011.

<https://ebookcentral.proquest.com/lib/kentuk/detail.action?docID=787253>

3

Hennessy, John L., Patterson, David A.,
Asanovic

, Krste. Computer architecture: a quantitative approach. 5th ed. Waltham, MA: : Morgan Kaufmann/Elsevier 2012.

4

Patterson, David A., Hennessy, John L. Computer organization and design: the hardware/software interface. Rev. 4th ed. Amsterdam: : Morgan Kaufmann 2012.

5

Chu, Pong P., MyiLibrary. FPGA prototyping by VHDL examples: Xilinx Spartan-3 version. Hoboken, N.J.: : Wiley-Interscience 2008.

<http://library.kent.ac.uk/cgi-bin/resources.cgi?url=http://lib.mylibrary.com?id=123733>

6

Chu, Pong P. FPGA prototyping by VHDL examples: Xilinx Spartan-3 version. Hoboken, N.J.: : Wiley-Interscience 2008.

7

Roth, Charles H., John, Lizy Kurian. Digital systems design using VHDL. 2nd ed. London: : Thomson 2008.

8

Rushton, Andrew. VHDL for logic synthesis. 3rd ed. Oxford: : Wiley-Blackwell 2011.

9

Parhami, Behrooz. Computer arithmetic: algorithms and hardware designs. 2nd ed. New York: : Oxford University Press 2010.

10

Ashenden, Peter J., Dawsonera. The designer's guide to VHDL. 3rd ed. Amsterdam: : Morgan Kaufmann 2008.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9780080568850>

11

Ashenden, Peter J. The designer's guide to VHDL. 3rd ed. Amsterdam: : Morgan Kaufmann Publishers 2008.

12

Pedroni, Volnei A. Digital electronics and design with VHDL. Oxford: : Elsevier Science [distributor] 2008.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9780080557557>

13

Pedroni, Volnei A. Digital electronics and design with VHDL. Oxford: : Morgan Kaufmann 2008.

14

Salemi, Ray. FPGA simulation: a complete step-by-step guide. [S.l.: s.n.] 2009.

15

Hamblen, James O., Hall, Tyson S., Furman, Michael D. Rapid prototyping of digital systems . SOPC ed. New York: : Springer 2008.

16

Sass, Ronald, Schmidt, Andrew G. Embedded systems design with platform FPGAs: principles and practices. Amsterdam: : Morgan Kaufmann 2010.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9780080921785>

17

Sass, Ronald, Schmidt, Andrew G. Embedded systems design with platform FPGAs: principles and practices. Amsterdam: : Morgan Kaufmann 2010.

18

Athanas, Peter, Pnevmatikatos, Dionisios, Sklavos, Nicolas. Embedded Systems Design with FPGAs. 2013th ed. New York, NY: : Springer 2012.

19

Harris, David Money, Harris, Sarah L. Digital design and computer architecture. 2nd ed. Oxford: : Morgan Kaufmann 2012.

20

Swartzlander, Earl E., Lemonds, Carl. Computer arithmetic: a complete reference. London: : Springer 2008.

21

Kilts, Steve. Advanced FPGA design: architecture, implementation, and optimization. Hoboken, N.J.: : Wiley-Interscience 2007.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9780470127889>

22

Arora, Mohit. The art of hardware architecture: design methods and techniques for digital circuits. New York, NY: : Springer 2012.

23

Arora, Mohit. The art of hardware architecture: design methods and techniques for digital circuits. London: : Springer 2011.

24

Samanta, Swagata., Paik, Soumi. & Chakrabarti, Amlan. Design & Implementation of Digital Image Processing using FPGA: FPGA-based digital image processing. [Place of publication not identified]: : LAP LAMBERT Academic Publishing

25

Sadrozinski, H. F.-W., Wu, Jinyuan. Applications of field-programmable gate arrays in scientific research. London: : Taylor & Francis 2010.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9781439841341>

26

Bezerra, Eduardo. Reconfigurable Systems in Space Instrumentation. Colne: : LAP Lambert Academic Publishing AG & Co KG 2010.

27

Nisan, Noam, Schocken, Shimon. The elements of computing systems: building a modern computer from first principles. Cambridge, Mass: : MIT 2008.

28

Stallings, William. Operating systems: internals and design principles. 7th ed. Boston, [Mass.]: : Pearson 2012.

29

Han, Kyungtae. Transforming Floating-Point Algorithms to Fixed-Point Implementations. Saarbrücken: : VDM Verlag Dr. Muller Aktiengesellschaft & Co. KG 2009.

30

Goraya, Muhammad Aitsam-ul-Haq, Sial, Shoaib, Arshad, S. Hardware Implementation of Digital Satellite Receiver. Saarbrücken: : VDM Verlag Dr. Muller Aktiengesellschaft & Co. KG 2010.

31

Keller, Rainer, Kramer, David, Weiss, Jan-Philipp. Facing the Multicore-Challenge: Aspects of New Paradigms and Technologies in Parallel Computing. 1st Edition. Berlin, Heidelberg: : [publisher not identified] 2010.

32

Koch, Dirk. Partial Reconfiguration on FPGAs: Architectures, Tools and Applications. 2012th ed. New York, NY: : Springer 2012.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9781461412250>

33

Platzner, Marco, Teich,
Ju

rgen, Wehn, Norbert. Dynamically reconfigurable systems: architectures, design methods

and applications. Dordrecht: : Springer 2010.

34

Anderson, David, Padgett, Wayne T., Moura, Jose. Fixed-Point Signal Processing. San Rafael: : Morgan & Claypool Publishers 2008.

35

Gaillardon, Pierre-Emmanuel, O'Connor, Ian, Clermidy, Fabien. Disruptive Logic Architectures and Technologies: From Device to System Level. 2012th ed. New York, NY: : Springer 2012.
<http://www.vlebooks.com/vleweb/product/openreader?id=KentUniv&isbn=9781461430582>